

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
**(Under 37 CFR 1.97(b) or 1.97(c))**

Docket No.  
BUR920030025US1

In Re Application Of: **ROBERT GEFFKEN ET AL.**

OCT 27 2003

Serial No.  
10/605,440

Filing Date  
09/30/2003

Examiner  
UNKNOWN

Group Art Unit  
UNKNOWN

Title: **ADJUSTABLE SELF-ALIGNED AIR GAP DIELECTRIC FOR LOW CAPACITANCE WIRING**

Address to:

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**37 CFR 1.97(b)**

1.  The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

2.  The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

the statement specified in 37 CFR 1.97(e);

**OR**

the fee set forth in 37 CFR 1.17(p).

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**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- A check in the amount of \_\_\_\_\_ is attached.
- The Director is hereby authorized to charge and credit Deposit Account No. 09-0456 as described below.
  - Charge the amount of \_\_\_\_\_
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I certify that this document and fee is being deposited on 10/23/2003 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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 Signature

Anthony J. Canale, Reg. No. 51,526

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Dated: 10/23/2003

CC:

<b>INFORMATION DISCLOSURE CITATION</b> <small>(Use several sheets if necessary)</small>					Docket Number (Optional) <b>BUR920030025US1</b>	Application Number <b>10/605,440</b>		
					Applicant(s) <b>Robert Geffken et al.</b>			
					Filing Date <b>09/30/2003</b>	Group Art Unit <b>Unknown</b>		
<b>U.S. PATENT DOCUMENTS</b>								
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
<b>FOREIGN PATENT DOCUMENTS</b>								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
<b>OTHER DOCUMENTS</b> <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>								
		<b>V. Arnal, J. Torres, P. Gayet, R. Gonella, P. Spinelli, M. Guillermet, J-P Reynard, G.C. Verove; "Integration of a 3 Level CuSiO<sub>2</sub> Air Gap Interconnect for Sub 0.1 micron DMOS Technologies;" IEEE, 6/01, pages 298-300.</b>						
EXAMINER				DATE CONSIDERED				
<b>EXAMINER:</b> Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								